

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics & Communication Engineering**  
**Time Table for the Academic Year 2015-16**  
**II B.Tech II Semester ECE Section-B**

Room No : 304 North Block (NB)

w.e.f : 21-12-2015

Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630
Monday	PEE	<-----GS LAB----->		LUNCH	ES	ECA	EMTL
Tuesday	PDC	EMTL	PEE		PDC(G1-304) ECA(G2-309) EMTL(G3-311)	GS	ECA
Wednesday	<-----ECPC/ ET LAB----->				PDC	ES	DDV
Thursday	ECA	EMTL(G1-304) PDC(G2-309) ECA(G3-311)	PDC		ES	DDV	PEE
Friday	DDV	ECA	EMTL		<----- ET/ ECPC LAB----->		
Saturday	ES	DDV	EMTL		PEE	ECA(G1-304) EMTL(G2-309) PDC(G3-311)	PDC

S.No	Subject		Faculty	Dept
1	PEE	Principles of Electrical Engineering	T.Vinay Kumar	EEE
2	ECA	Electronic Circuit Analysis	G.Veena	ECE
3	PDC	Pulse and Digital Circuits	Neha Jain	ECE
4	ES	Environmental Studies	Y. Swetha	BS
5	EMTL	Electromagnetic Theory and Transmission Lines	A.Navila	ECE
6	DDV	Digital Design using Verilog HDL	K.Amtul Salam	ECE
7	GS	Gender Sensitization	Ms. Fathima Parveen	ENGLISH
8	ET LAB	Electrical Technology Lab	Pramod Singh, S. Deepthi/T. Vinay Kumar	EEE
9	ECPC LAB	Electronic Circuits and Pulse Circuits lab	Neha Jain, G.Veena	ECE
10	GS LAB	Gender Sensitization Lab	Ms. Fathima Parveen	ENGLISH

Tutorial Groups :

G1: 461-486

G2: 487-4B0

G3: 4B1-L424